

AMENDMENTS TO THE SPECIFICATION

Please replace Paragraphs [0002] and [0003] with the following paragraphs rewritten in amendment format:

[0002] This patent application claims priority under 35 USC § 119(e) to provisional patent application, serial number 60/453,241, ~~Docket Number QE1056-USPROV~~, filed on March 10, 2003, incorporated herein by reference in its entirety.

[0003] This patent application is also related to the following U.S. patent applications filed on March 10, 2003, assigned to the same assignee, incorporated herein by reference in their entirety:

"METHOD AND SYSTEM FOR SUPPORTING MULTIPLE EXTERNAL SERIAL PORT DEVICES USING A SERIAL PORT CONTROLLER IN AN EMBEDDED DISK CONTROLLER", ~~Docket Number QE1042-US~~, Serial No. 10/385,039, with MICHAEL R. SPAUR AND IHN KIM as inventors;

"METHOD AND SYSTEM FOR AUTOMATIC TIME BASE ADJUSTMENT FOR DISK DRIVE SERVO CONTROLLERS", ~~Docket NUMBER QE1040-US~~, SERIAL No. 10,384,992, with MICHAEL R. SPAUR AND RAYMOND A. SANDOVAL as inventors;

"METHOD AND SYSTEM FOR USING AN EXTERNAL BUS CONTROLLER IN EMBEDDED DISK CONTROLLERS" Serial No. 10,385,056, ~~Docket no. QE1035-US~~ with GARY R. ROBECK, LARRY L. BYERS, JOSEBA M. DESUBIJANA, and FREDARICO E. DUTTON as inventors.

"METHOD AND SYSTEM FOR USING AN INTERRUPT CONTROLLER IN EMBEDDED DISK CONTROLLERS", Serial No. 10/384,991, ~~Docket No. QE1039.US~~, with DAVID M. PURDHAM, LARRY L. BYERS and ANDREW ARTZ as inventors.

"METHOD AND SYSTEM FOR MONITORING EMBEDDED DISK CONTROLLER COMPONENTS", Serial No. 10/385,042, ~~Docket Number QE1038.US~~, with LARRY L. BYERS, JOSEBA M. DESUBIJANA, GARY R. ROBECK, and WILLIAM W. DENNIN as inventors.

"METHOD AND SYSTEM FOR COLLECTING SERVO FIELD DATA FROM PROGRAMMABLE DEVICES IN EMBEDDED DISK CONTROLLERS", Serial No. 10/385,405, ~~Docket NO. QE1041.US~~, with MICHAEL R. SPAUR AND RAYMOND A. SANDOVAL as inventors.

"METHOD AND SYSTEM FOR EMBEDDED DISK CONTROLLERS", Serial No. 10/385,022 ~~Docket NO. QE1034.US~~, with Larry L. Byers, Paul B. Ricci, Joseph G. Kriscunas, Joseba M. Desubijana, Gary R. Robeck, David M. Purdham and Michael R. Spaur as inventors.

Please replace Paragraphs [00025], [00035], [00036], [00037], [00040], and [00053] with the following paragraphs rewritten in amendment format:

[00025] ~~Figure 3 is a schematic~~ Figures 3-(i) through 3-(iv) are schematics showing plural signals to and from servo-controller and servo controller interface;

[00035] ~~Figure 11 shows~~ Figures 11-(i) and 11-(ii) show a table describing various output signals from the servo controller to the servo controller interface, according to one aspect of the present invention;

[00036] ~~Figures 12A-12B~~ 12A-(i), 12A-(ii), and 12B-(i) through 12B-(iv) show schematic diagrams of the servo controller and the servo controller interface according to one aspect of the present invention;

[00037] ~~Figure 12C shows~~ Figures 12C-(i) through 12C-(iv) show a schematic of the speed match FIFO, according to one aspect of the present invention;

[00040] ~~Figure 15 shows~~ Figures 15-(i) and 15-(ii) show a timing diagram with the APB Bus write conflicting with the DSP write operations;

[00053] ~~Figure 27 provides~~ Figures 27-(i) and 27-(ii) provide a table of various bits that are used by a control register for SCI, according ~~to~~ to one aspect of the present invention;